AUG 1 8 2003

## **EXPEDITED PROCEDURE - EXAMINING GROUP 2816**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Rajendran Nair et al.

Examiner:

Anh Quan Tra

Serial No .:

09/460,742

Group Art Unit:

884.229US1

2816

Filed: Title:

December 14, 1999

Docket No.:

Assignee:

DEVICE AND METHOD FOR CONTROLLING VOLTAGE VARIATION **Intel Corporation** 

## **AMENDMENT & RESPONSE UNDER 37 C.F.R. 1.116**

Mail Stop AF Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

This paper is being filed in response to the final office action mailed May 15, 2003.

This response is prepared in the "revised amendment format" announced by the Deputy

Commissioner for Patent Examination Policy on June 30, 2003, effective July 30, 2003.

Please amend the above-referenced patent application as follows.